#3

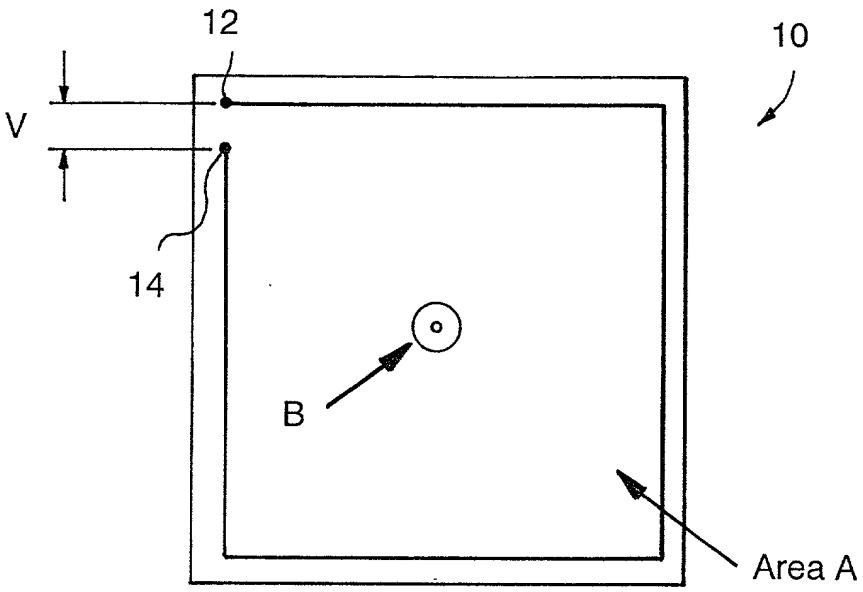

Figure 1

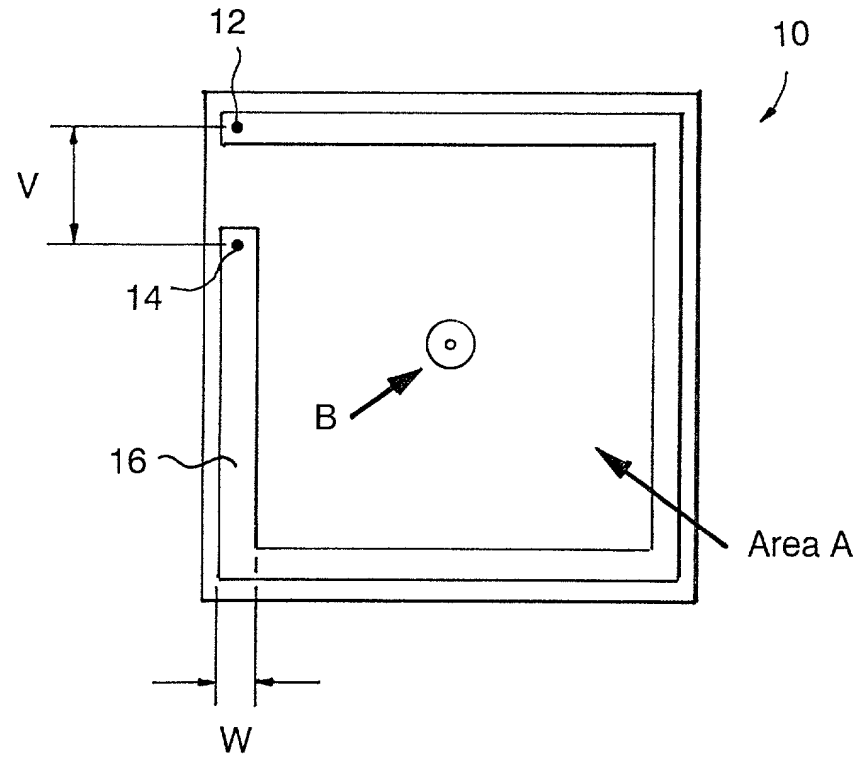

Figure 2

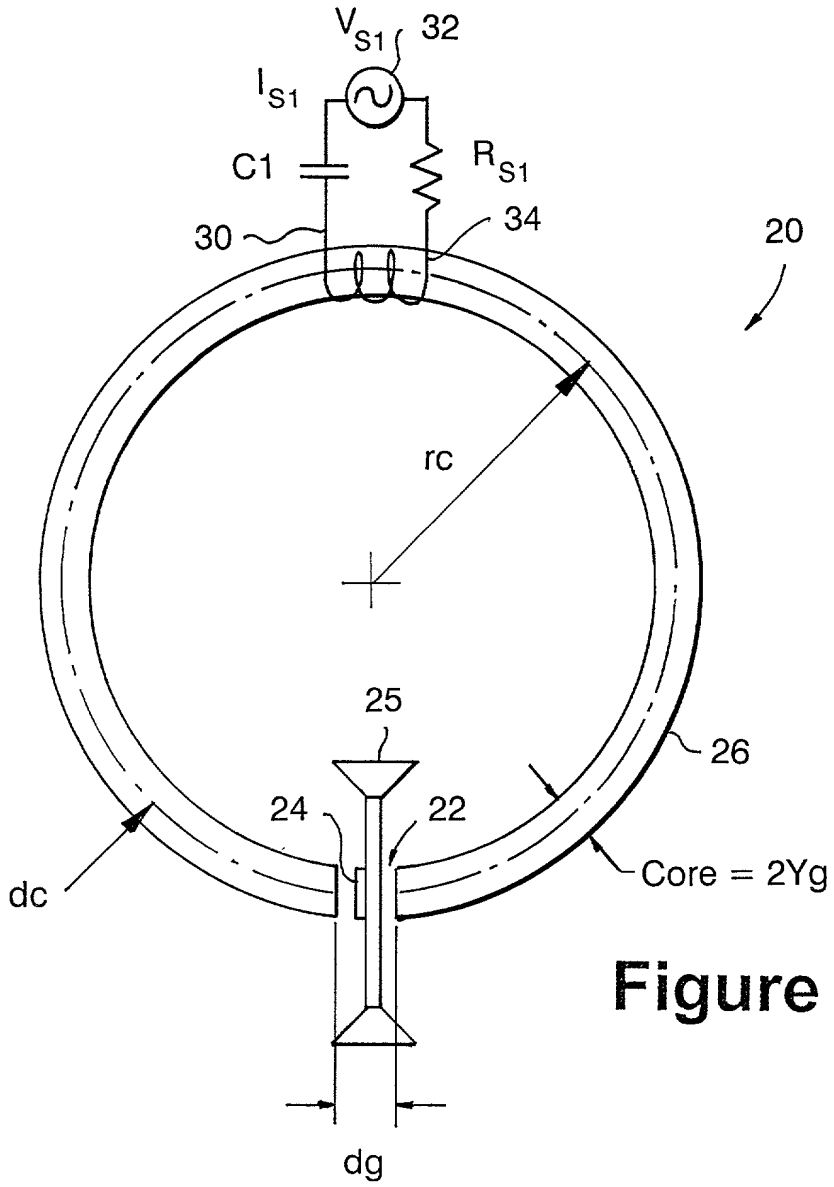

Figure 3

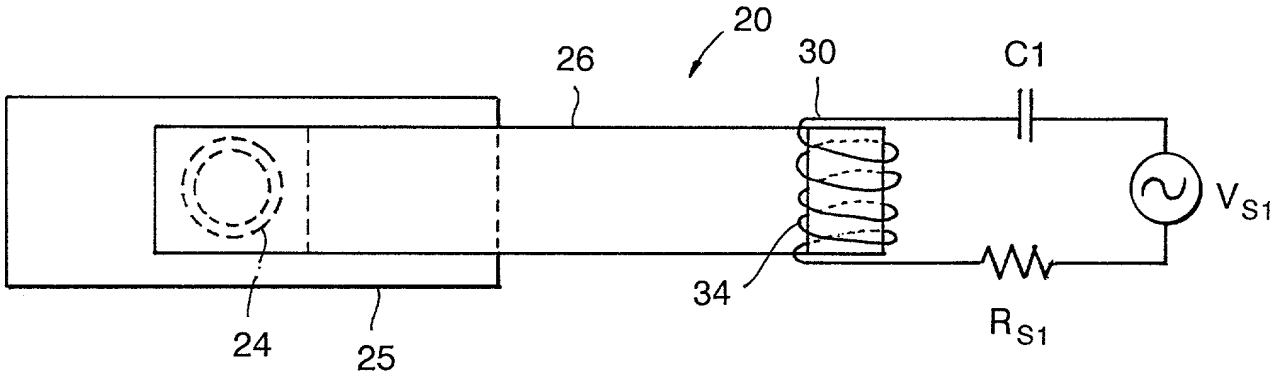

Figure 3a

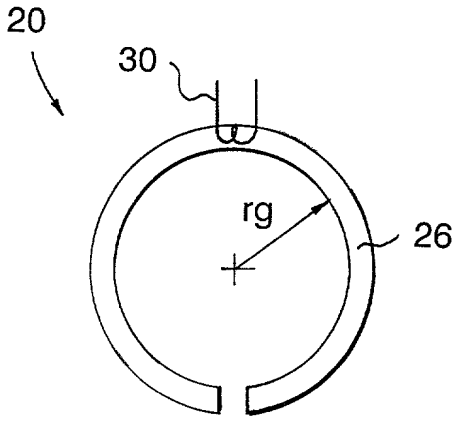

Figure 4

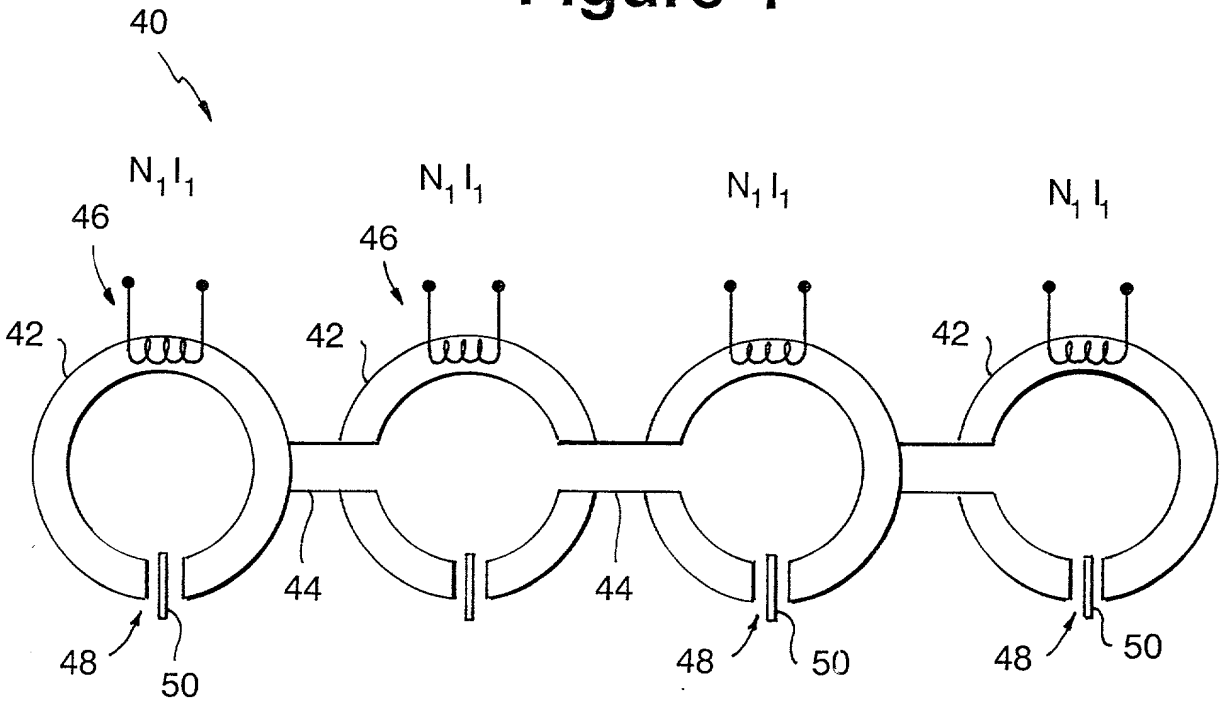

Figure 5

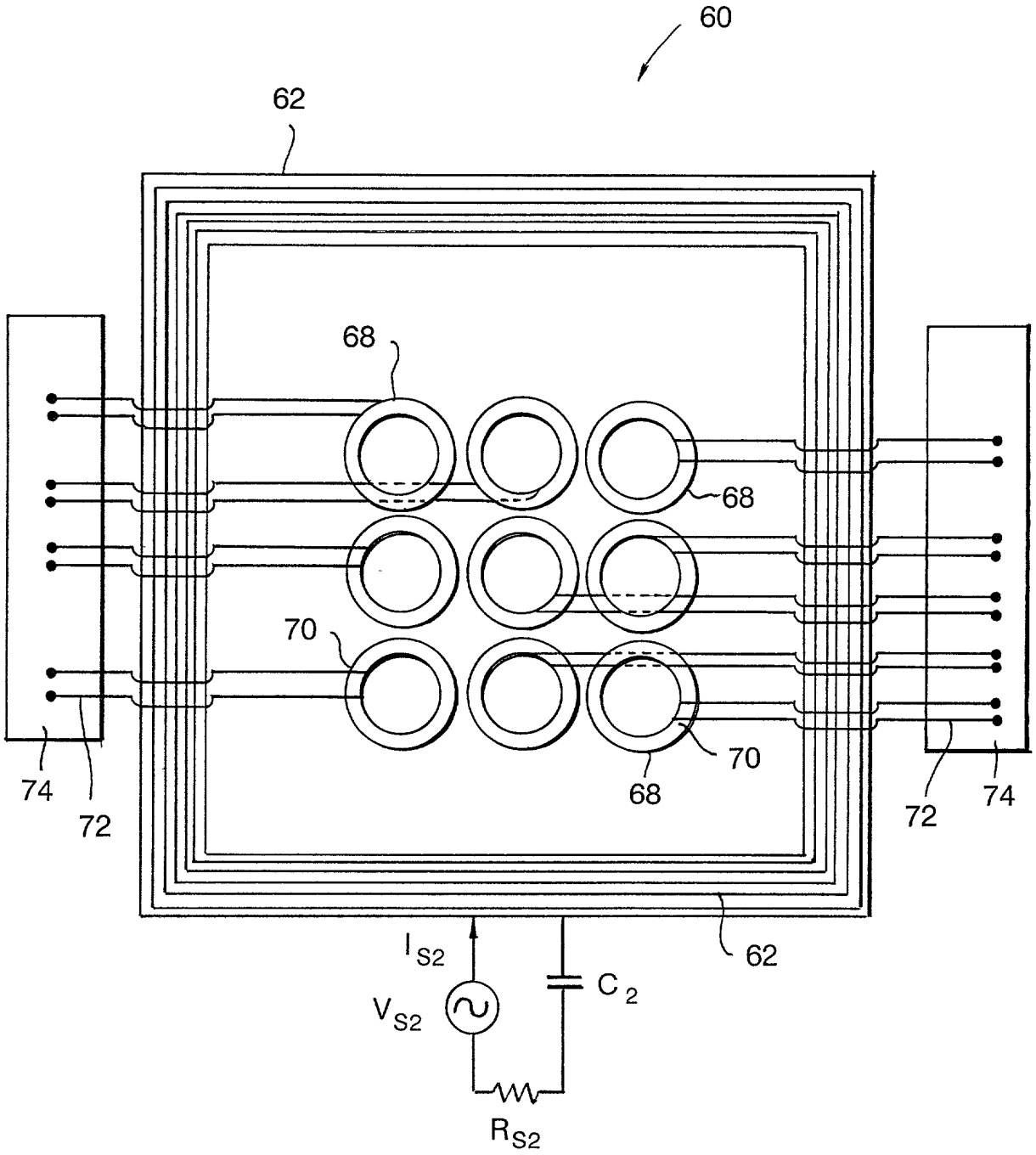

Figure 6

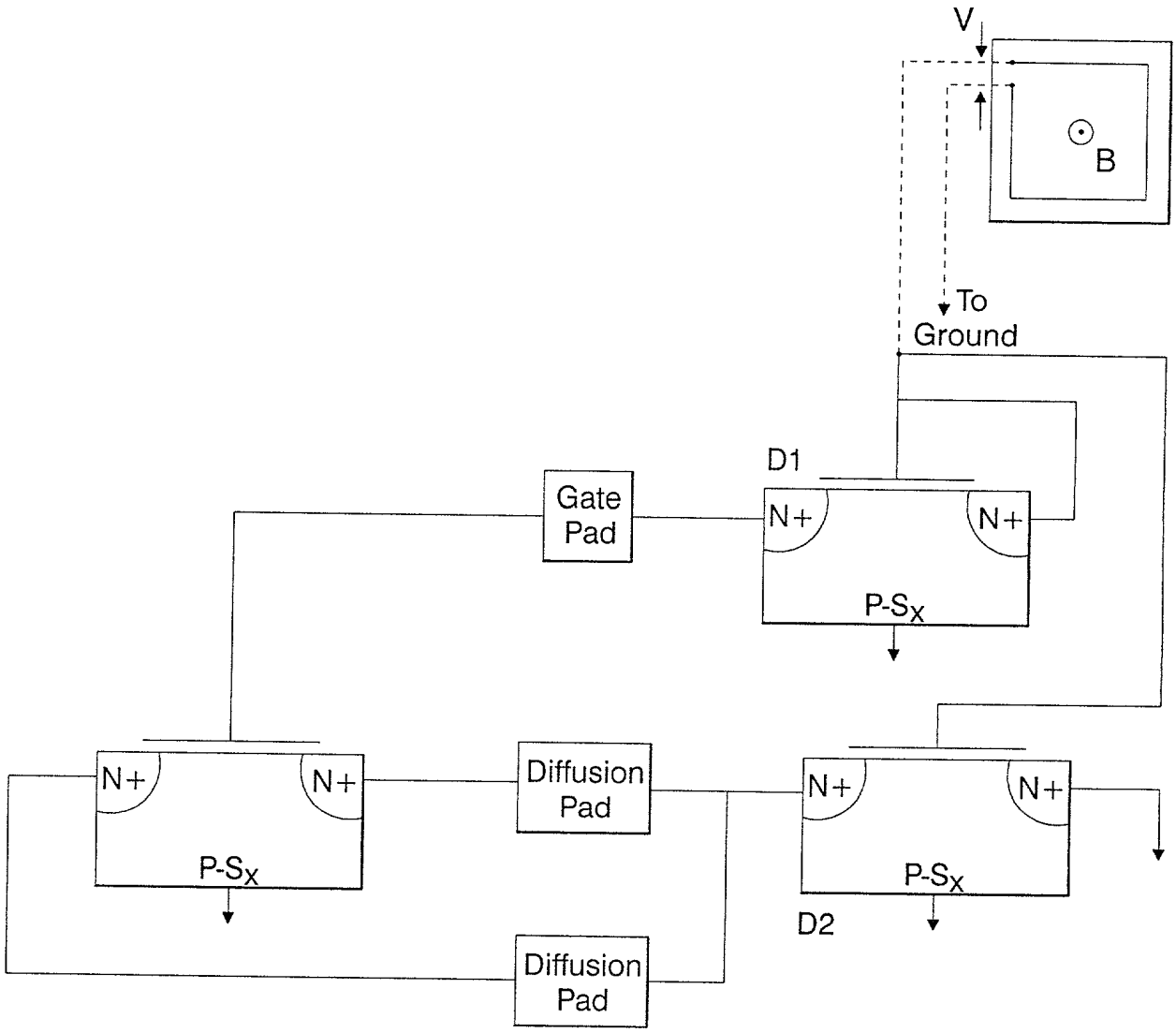
V
B
To
Ground
D1
Gate
Pad
N+
N+
P-Sx
N+
N+
Diffusion
Pad
P-Sx
N+
N+
P-Sx
D2
Diffusion
Pad

Figure 7

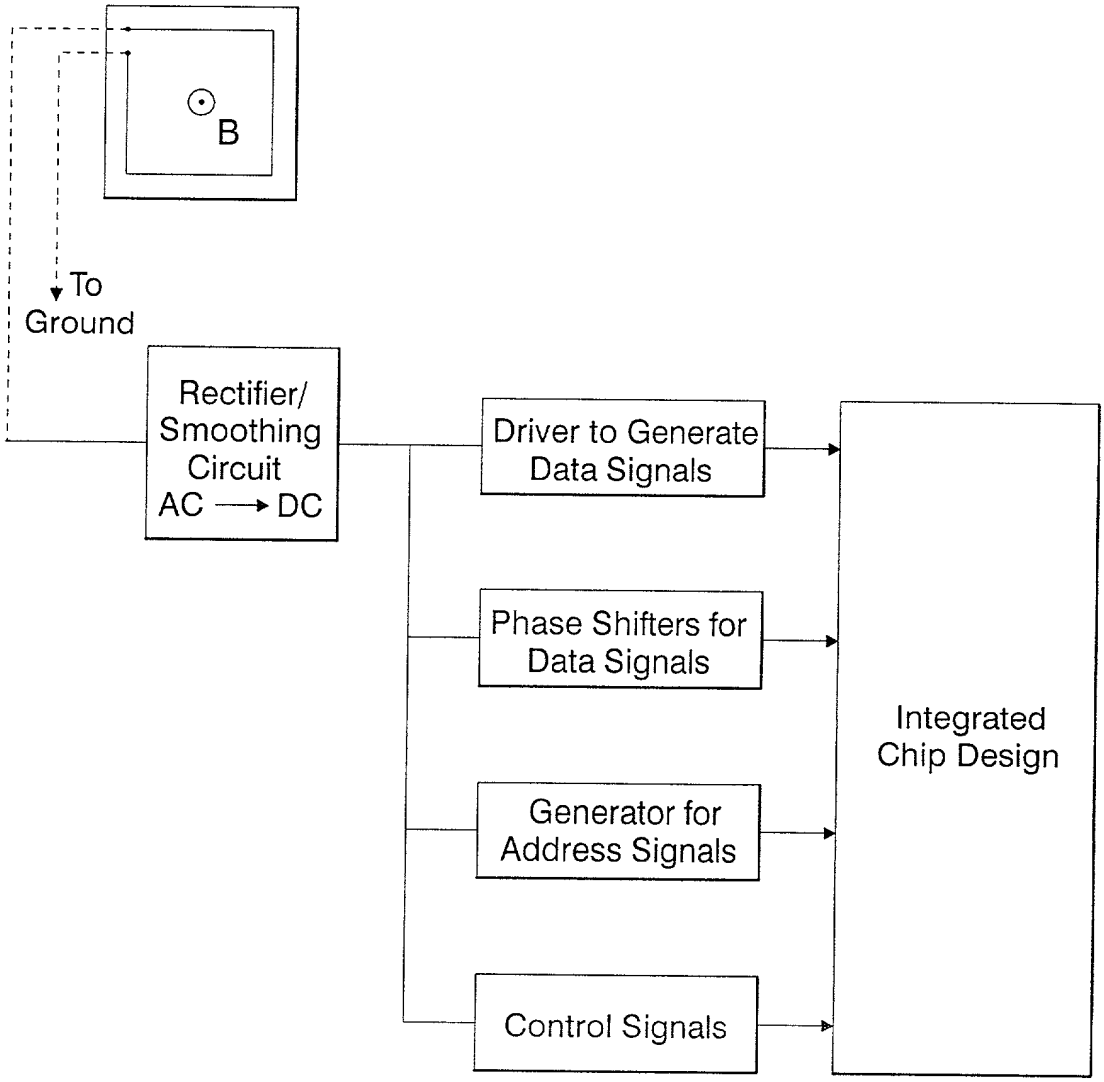
B
To
Ground
Rectifier/
Smoothing
Circuit
AC → DC
Driver to Generate
Data Signals
Phase Shifters for
Data Signals
Generator for
Address Signals
Control Signals
Integrated
Chip Design

Figure 8

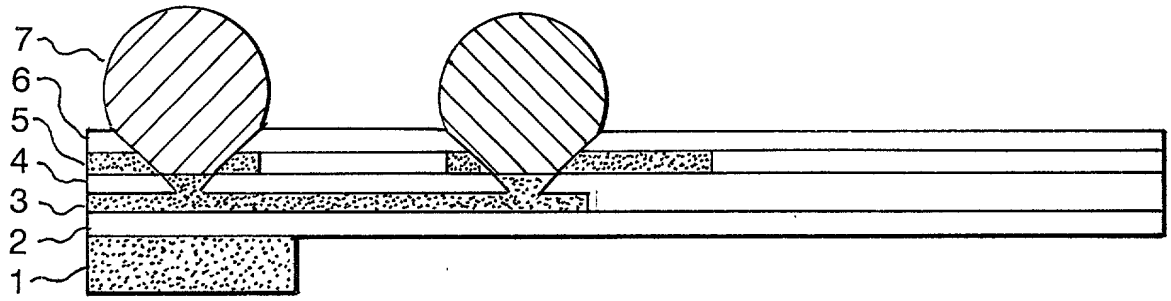

Figure 9

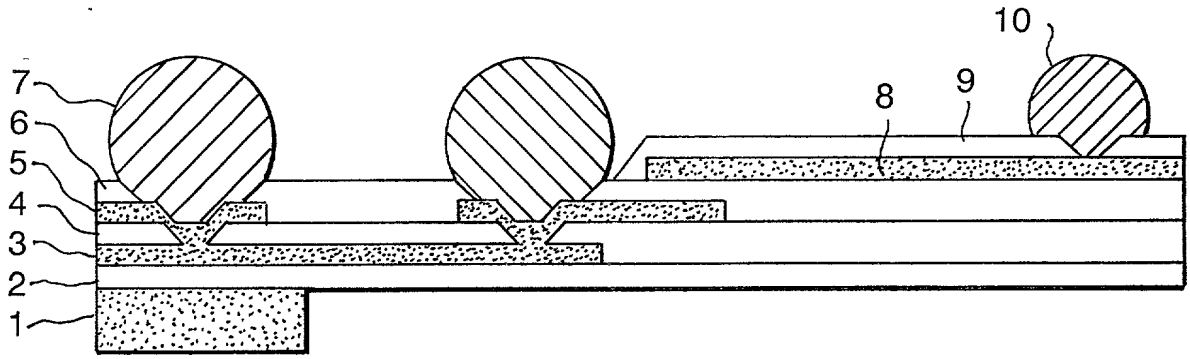

Figure 10